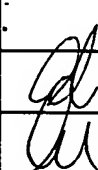

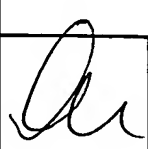
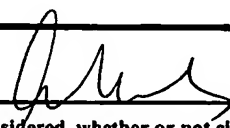
				Docket Number (Optional) 17119		Application Number 10/685,030		
				Applicant(s) Yoshinori Muramatsu				
				Filing Date Oct. 14, 2003		Group Art Unit 2817		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
U.S. PATENT APPLICATION PUBLICATIONS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		11-330852	11/30/99	Japan			✓	
		2001-203603	7/27/01	Japan			✓	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
		Sjoland, H., "Improved Switched Tuning of Differential CMOS VCOs", IEEE Transactions on Circuits and Systems II, Analog and Digital Signal Processing, U.S.A., May 2002, Vol. 49, Issue 5, pp. 352-355						
EXAMINER				DATE CONSIDERED				
				5/26/06				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

